



6W Q-Band Power Amplifier 39GHz~48GHz



Features

- Wideband Solid State Power Amplifier
- Psat: 37dBm Typical
- Gain: 32dB

Typical Applications

- Wireless Infrastructure
- Short Haul / High Capacity Links
- RF Microwave and Vsat
- Military & Aerospace Applications
- Test Instrumentation

Electrical Specifications, $T_A=25^\circ\text{C}$, $V_{CC} = +36\text{V}$

Parameter	Min	Typ	Max	Units
Frequency Range	39 ~ 48			GHz
Gain		32		dB
Input Return Loss		-15	-10	dB
Output Return Loss		-18	-10	dB
Output Power For 1dB Compression (P1dB)		33		dBm
Saturated Power (Psat)		37		dBm
Supply Current (Vcc=+36V)		2.3		A
Power Supply		36		V
Isolation S12		65		dB
RF Input Power		0		dBm
Weight	800			g
Impedance	50			Ohms
Input / Output Connectors	2.4mm - Female			
Finish	Nickel Plated			
Material	Aluminum / Copper			

* P1dB, P3dB and Psat power testing signal: 200μs pulse width with 10% duty cycle.

* For average CW power testing, a 5dB back off from Psat is required unless water/oil cooling system is applied.

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Absolute Maximum Ratings	
Supply Voltage	+40 Vdc
RF Input Power (RFIN)	Psat – Gain Sat
Storage Temperature (°C)	-50 to +125

Note: Maximum RF input power is set to assure safety of amplifier. Input power may be increased at own risk to achieve full power of amplifier. Please reference gain and power curves.

Biasing Up Procedure	
Step 1	Connect Ground Pin
Step 2	Connect input and output with 50 Ohm source/load. (in band VSWR<1.9:1 or >10dB return loss)
Step 4	Connect +36V biasing
Power OFF Procedure	
Step 2	Turn off +36V biasing
Step 3	Remove RF connection
Step 4	Remove Ground.

Environmental Specifications and Test Standards

Parameter	Standard	Description
Operational Temperature	MIL-STD-39016	-45°C~+55°C (Case Temperature less than 85°C)
Storage Temperature		-50°C~+125°C
Thermal Shock		1 Hour@ -45°C → 1 Hour @ +85°C (5 Cycles)
Random Vibration		Acceleration Spectral Density 6 (m/s) Total 92.6 RMS
Electrical & Temperature Burn In		Temperature +85°C for 72 Hours
Shock		1. Weight >20g, 50g half sine wave for 11ms, Speed variation 3.44m/s 2. Weight <=20g, 100g Half sine wave for 6ms, Speed variation 3.75m/s 3. Total 18 times (6 directions, 3 repetitions per direction).
Altitude		Standard: 30,000 Ft (Epoxy Sealed Controlled Environment) Optional: Hermetically Sealed (60,000 ft. 1.0 PSI min)
Hermetically Sealed (Optional)	MIL-STD-883	MIL-STD-883 (For Hermetically Sealed Units)

Note: The operating temperature for the unit is specified at the package base. It is the user's responsibility to ensure the part is in an environment capable of maintaining the temperature within the specified limits

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Ordering Information	
Part No.	Description
RFLUPA39G48GB	39GHz~48GHz Power Amplifier

Amplifier Use

Ensure that the amplifier input and output ports are safely terminated into a proper 50 ohm load before turning on the power. Never operate the amplifier without a load. A proper 50 ohm load is defined as a load with impedance less than 1.9:1 or return loss larger than 10dB relative to 50 Ohm within the specified operating band width.

Power Supply Requirements

Power supply must be able to provide adequate current for the amplifier. Power supply should be able to provide 1.5 times the typical current or 1.2 times the maximum current (whichever is greater).

In most cases, RF - Lambda amplifiers will withstand severe mismatches without damage. However, operation with poor loads is discouraged. If prolonged operation with poor or unknown loads is expected, an external device such as an isolator or circulator should be used to protect the amplifier.

Ensure that the power is off when connecting or disconnecting the input or output of the amp.

Prevent overdriving the amplifier. Do not exceed the recommended input power level.

Adequate heat-sinking required for RF amplifier modules. Please inquire.

Amplifiers do not contain Thermal protection, Reverse DC polarity or Over voltage protection with the exception of a few models. Please inquire.

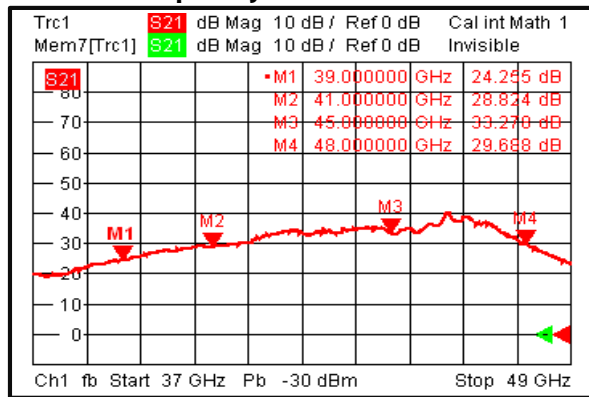
Proper electrostatic discharge (ESD) precautions are recommended to avoid performance degradation or loss of functionality.

What is not covered with warranty?

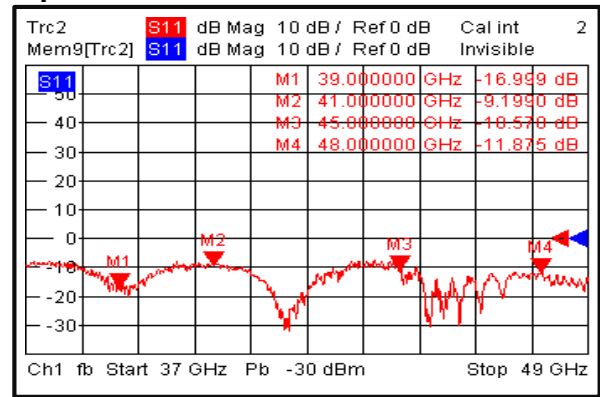
Each RF - Lambda amplifier will go through power and temperature stress testing. Since the die, ICs or MMICs are fragile, these are not covered by warranty. Any damage to these will NOT be free to repair.



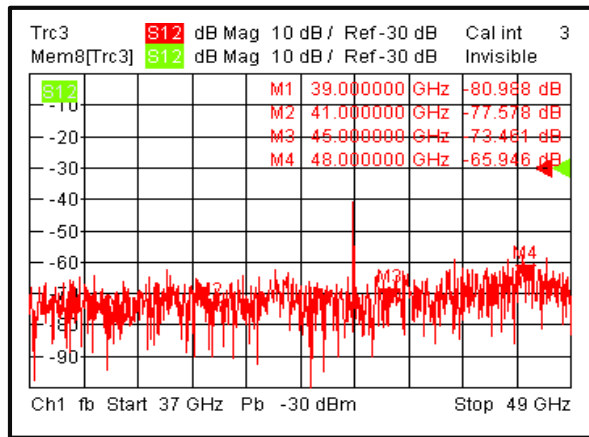
Gain vs. Frequency



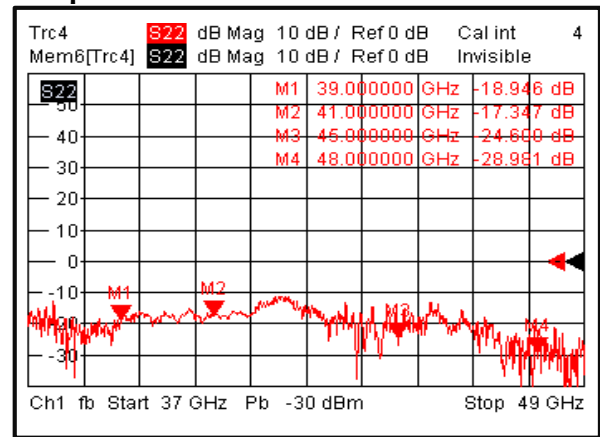
Input Return Loss



Isolation



Output Return Loss

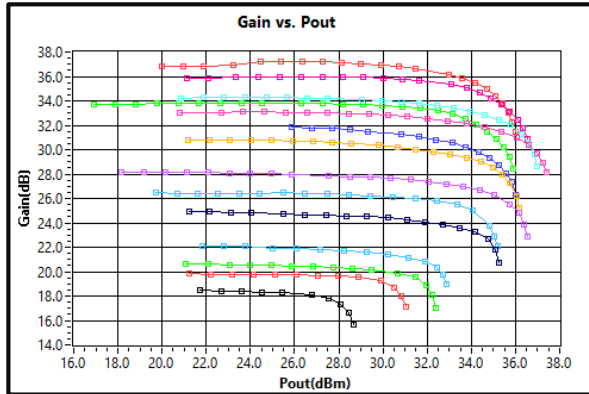


Note: Input/output return loss measurements include attenuators to protect equipment

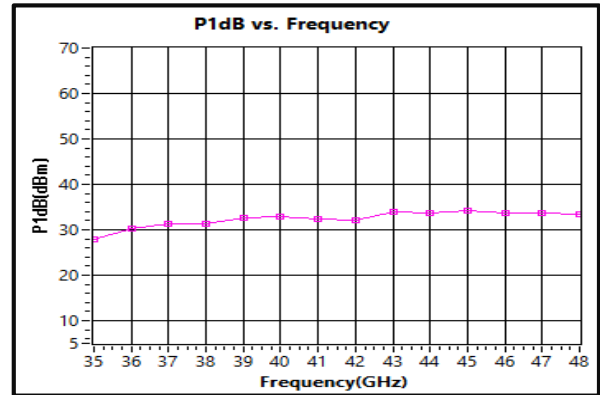
GW Q Band Power Amplifier 39GHz-48GHz



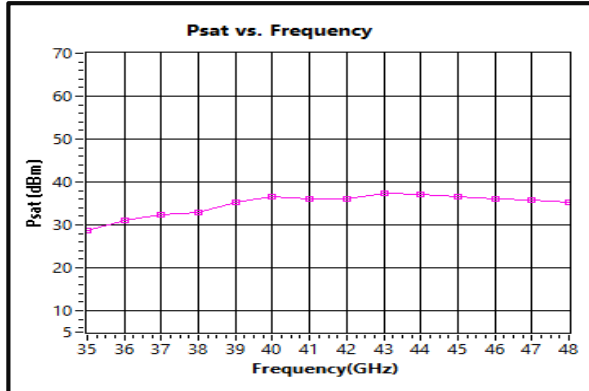
Gain vs. Output Power



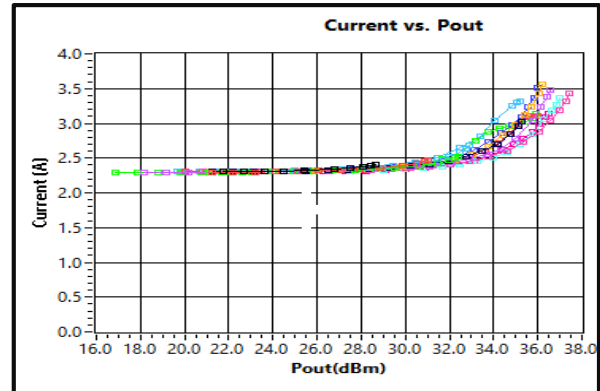
P1dB vs. Frequency



Psat vs. Frequency



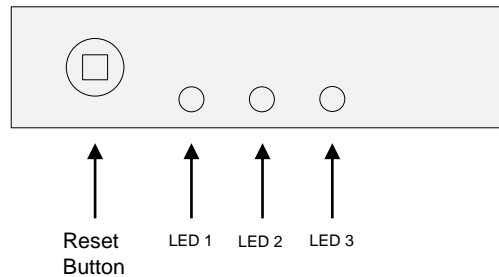
Current vs. Pout



35.000(GH)	
36.000(GH)	
37.000(GH)	
38.000(GH)	
39.000(GH)	
40.000(GH)	
41.000(GH)	
42.000(GH)	
43.000(GH)	
44.000(GH)	
45.000(GH)	
46.000(GH)	
47.000(GH)	
48.000(GH)	



Alarm Status Panel:

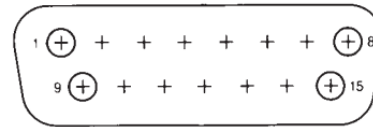


	Name	Function	Initial State	Description	Applied
	RESET	Control		Manual reset button to reset PA	Yes
LED 1	POWER	Indicator	RED Color	LED will light to RED color when supply power is applied	Yes
LED 2	ID	Indicator	GREEN Color	PA will shut down and latch this LED to a RED color when an imbalance in the drain current of the combining branches occurs or if a drain current limit is reached *	Yes
LED 3	TEMP	Indicator	GREEN Color	PA will shut down and latch this LED to a RED color when driven over temperature *	Yes

*LED needs to be manually reset to initial state by pressing RESET button



Protection Connector Table:



Pin #	Name	Function	Initial State	Description	Applied
1	Reset	Control		Resets PA when logic <u>LOW</u> is applied and released	Yes
2	Drain Disable	Control	LOW	Applying logic <u>HIGH</u> disables drains of amplifiers	No
3	Gate Disable	Control	LOW	Applying logic <u>HIGH</u> disables gates of amplifiers	Yes
4	RF IN Over	Indicator	LOW	Pin will be latched to logic <u>HIGH</u> when input signal is over limit	No
5	Temp Over	Indicator	LOW	Pin will be latched to logic <u>HIGH</u> when amplifier is driven over temperature	Yes
6	Current Over	Indicator	LOW	Pin will be latched to logic <u>HIGH</u> when drain current limit is reached	Yes
7	ID Imbalance	Indicator	LOW	Pin will be latched to logic <u>HIGH</u> when an imbalance in the drain current of the combining branches occurs	Yes
8	PA input power	Indicator		PA input power is represented by voltage	No
9	PA output power	Indicator		PA output power is represented by voltage	No
10	PA output reflection power	Indicator		PA output reflection power is represented by voltage	No
11	VSWR	Indicator	LOW	Pin will be latched to logic <u>HIGH</u> when output reflection is over limit	No
12	Temp Signal	Indicator		PA carrier case temperature is represented by voltage	Yes
13	+5V	Power Supply	+5V	+5V DC is supplied for reference	Yes
14	GND	Ground	GND	Ground	Yes
15	GND	Ground	GND	Ground	Yes

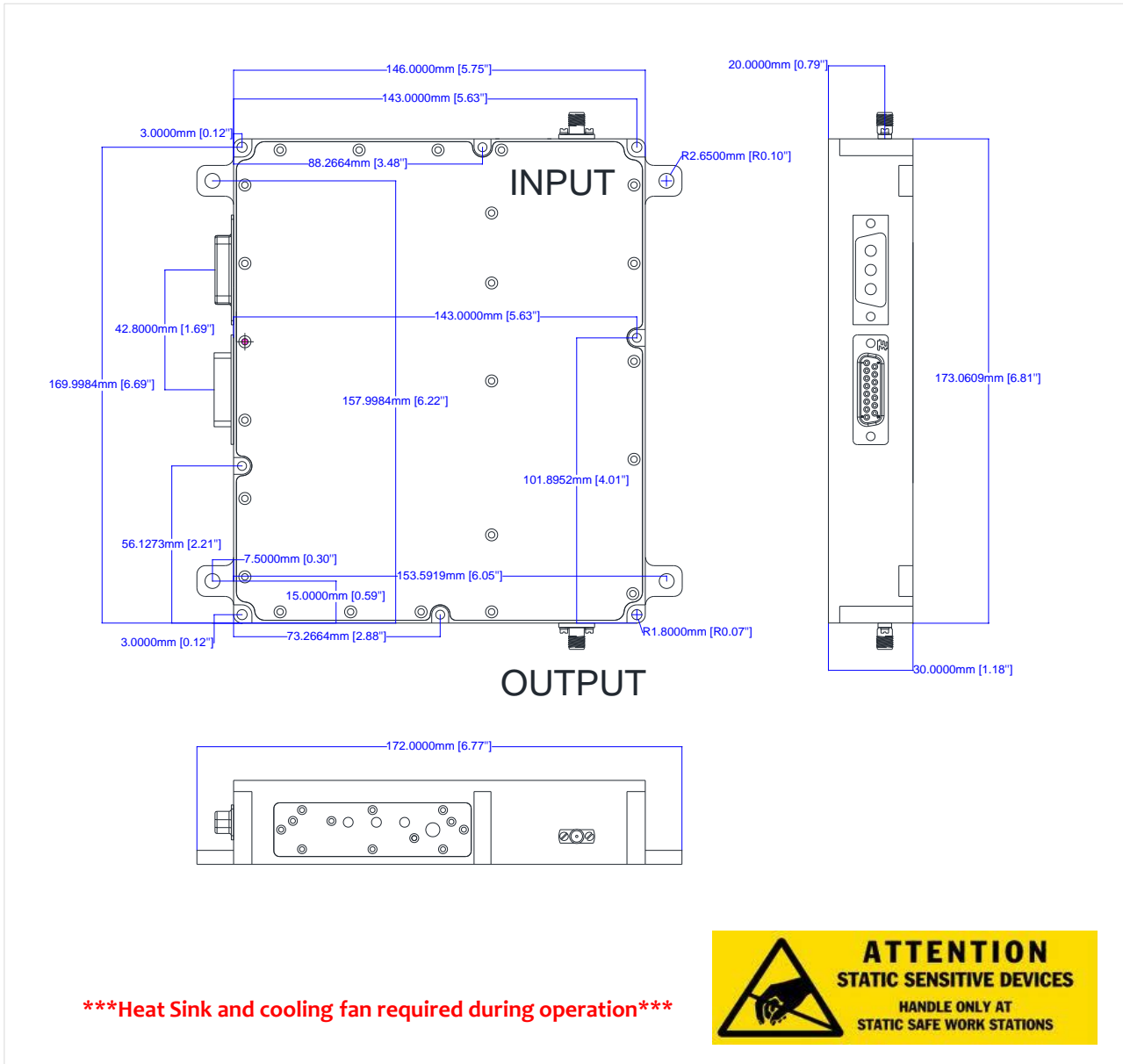
HIGH/LOW voltages are standard TTL signals:
0.0V-0.8V = LOW
2V-5V = HIGH

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Outline Drawing:

All Dimensions in mm [inches]



Important Notice

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